

### **ABSTRACT**

5 A multilayered integrated circuit and a method of designing a multilayered  
integrated circuit are provided. The circuit comprises at least two conductive  
layers and extraneous conductive lines placed in the conductive layers. The  
extraneous conductive lines are made of a material which is the same as the  
material in the conductive layers and have dimensions which are the same as the  
dimension of the material in the conductive layers. The extraneous conductive  
10 lines perform functions which are unnecessary to the operation of the integrated  
circuit and are undistinguishable from the functional conductive lines, thus  
burdening the work of a reverse engineer. The method of designing the  
multilayered circuit comprises a step of providing a computer generated  
representation of the extraneous conductive lines.